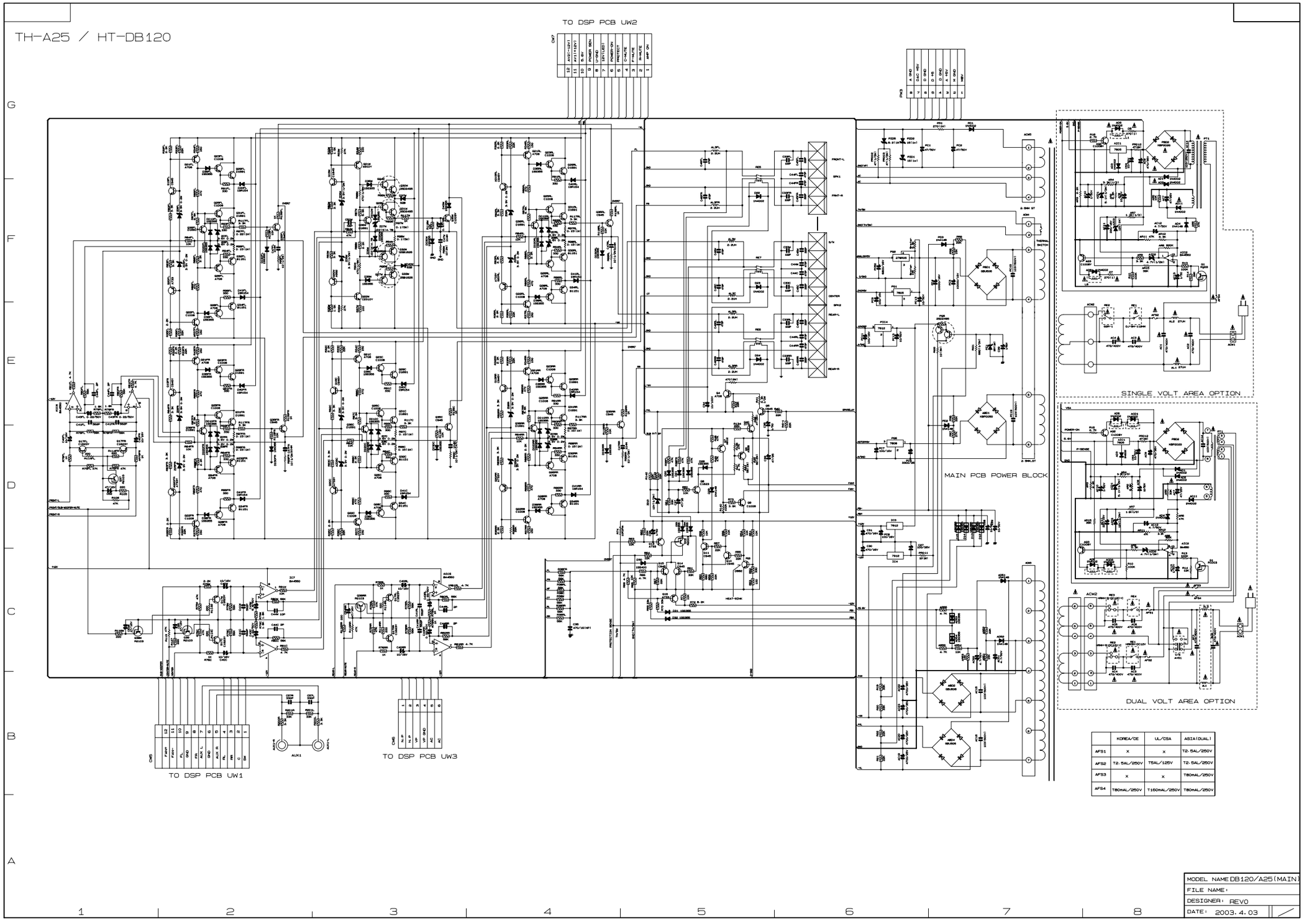
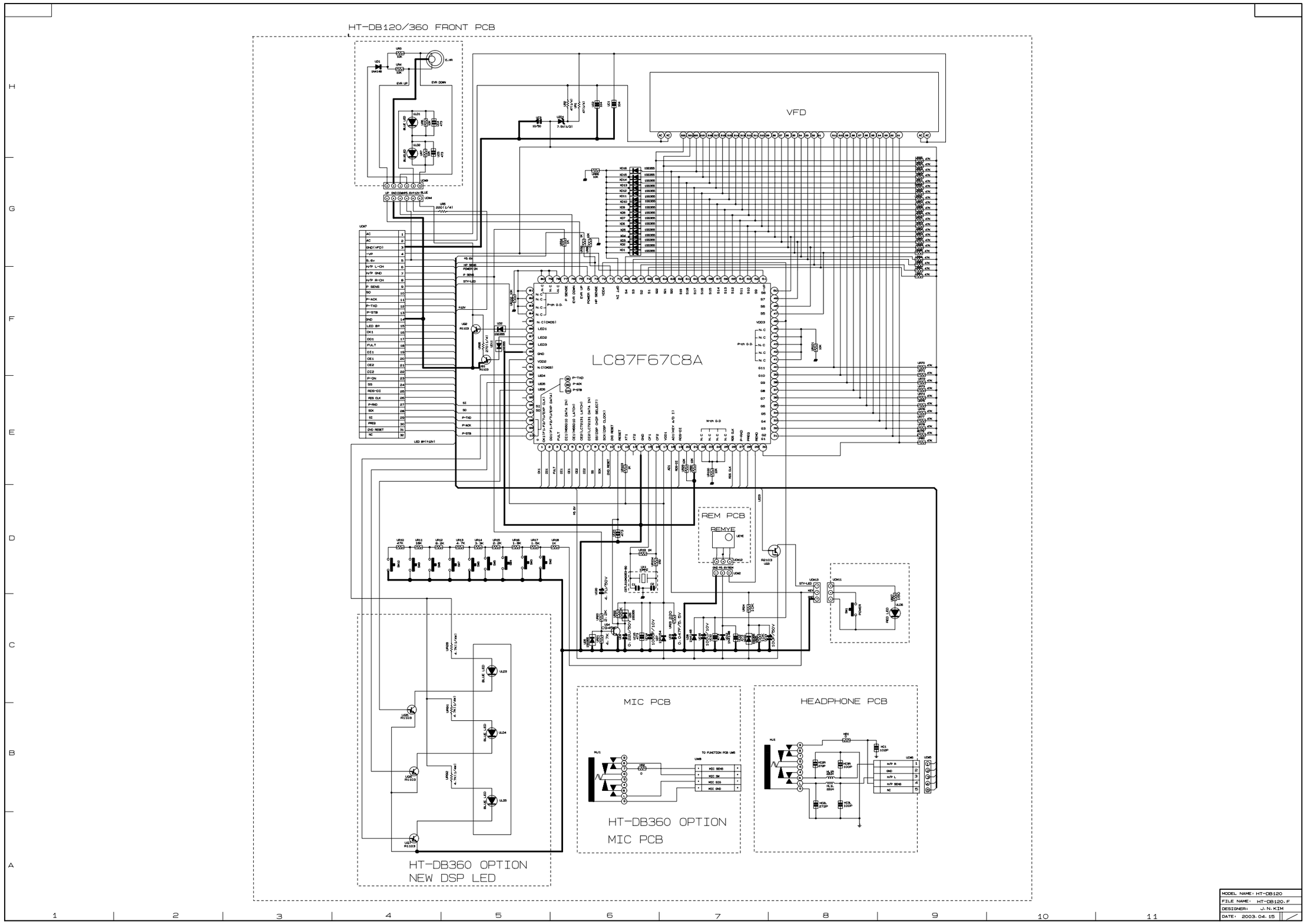
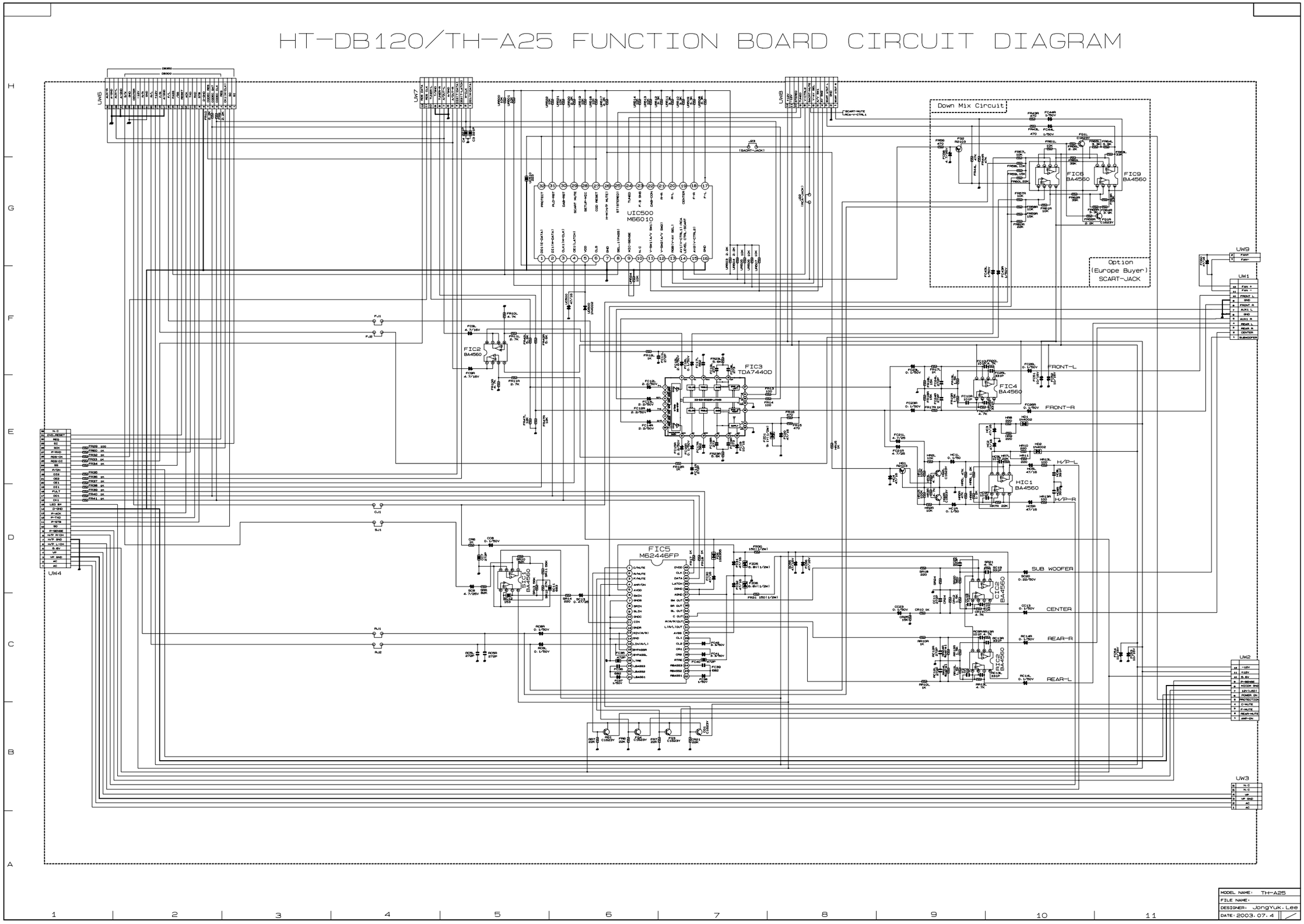
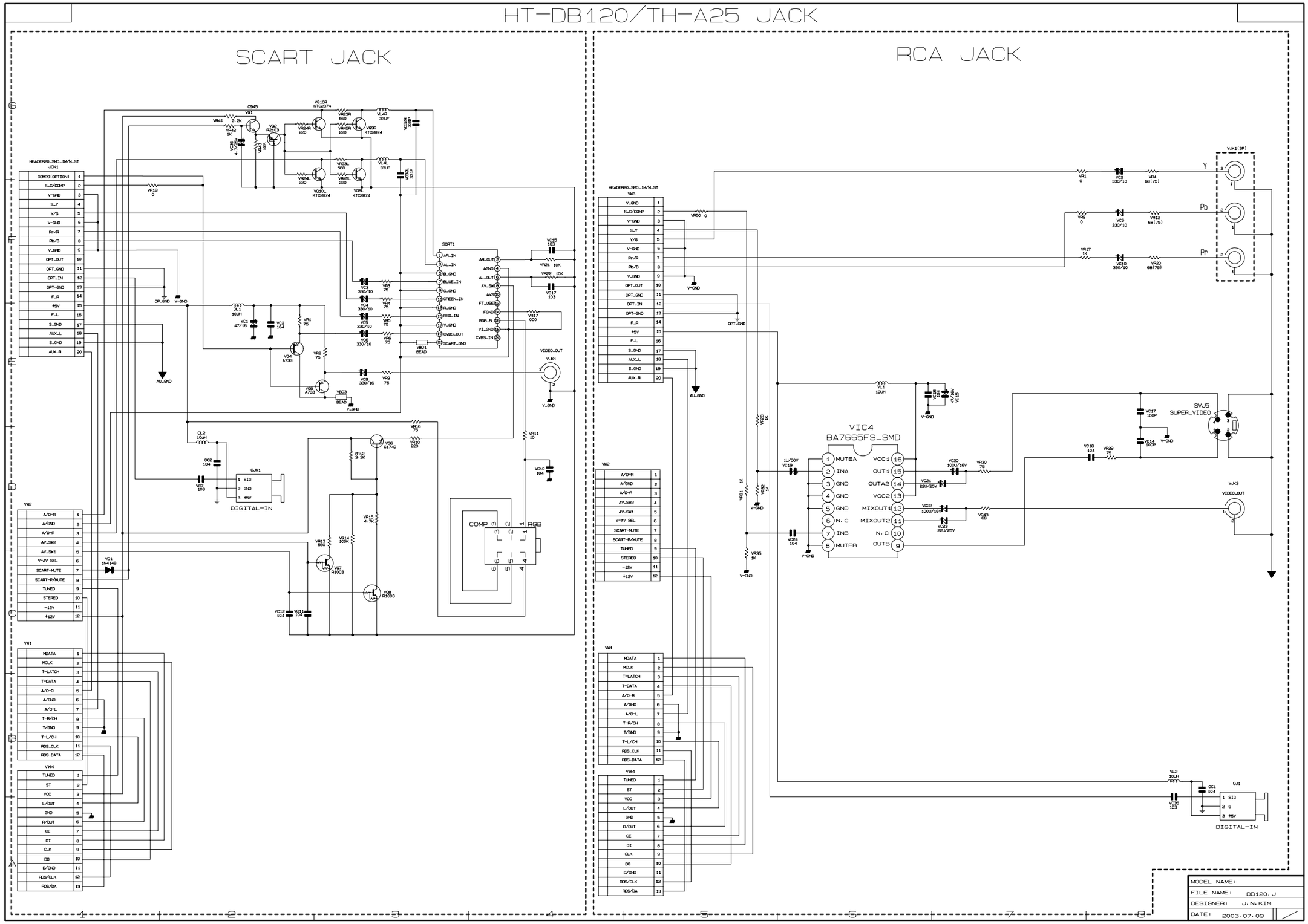
7-1 MAIN

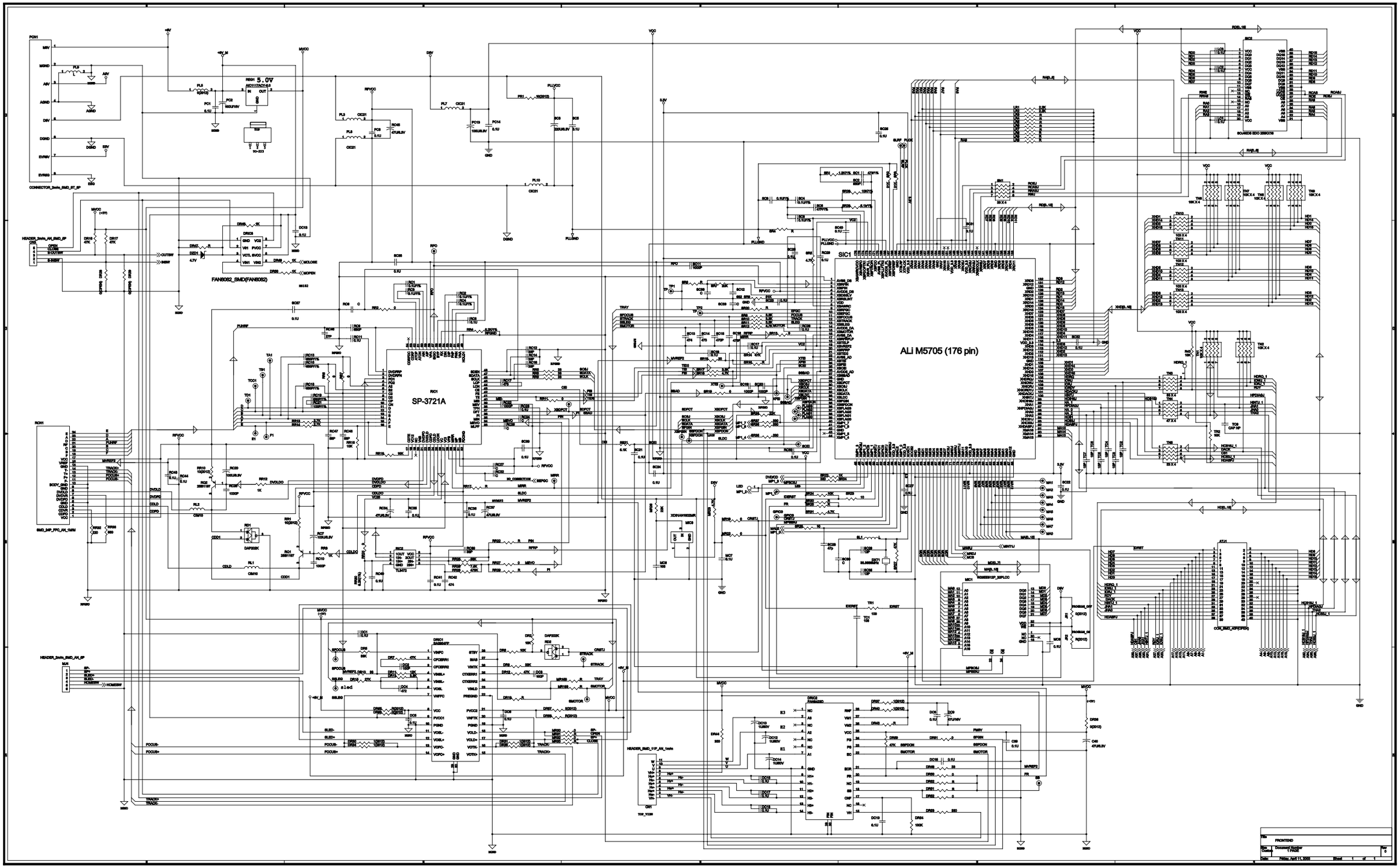

TH-A25 / HT-DB120
TO DSP PCB UW2
TO DSP PCB UW1
TO DSP PCB UW3
MAIN PCB POWER BLOCK
SINGLE VOLT AREA OPTION
DUAL VOLT AREA OPTION
KOREA/CE
UL/CSA
ASIA(DUAL)
AFS1
AFS2
AFS3
AFS4
T2.5AL/250V
T5AL/125V
T800mAL/250V
T160mAL/250V
MODEL NAME DB120/A25(MAIN)
FILE NAME:
DESIGNER: REV0
DATE: 2003.4.03

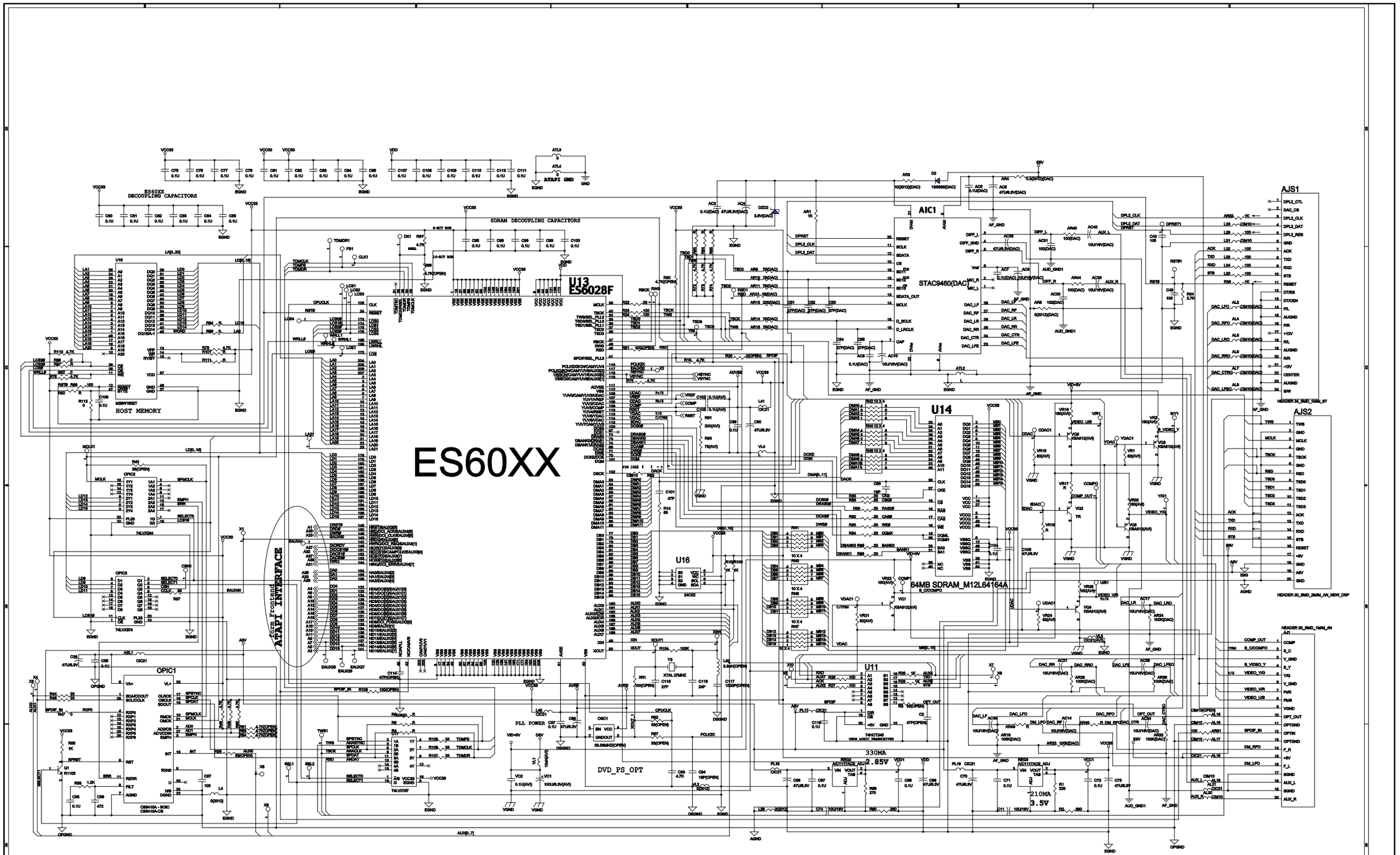

HT-DB120/360 FRONT PCB
VFD
LC87F67C8A
REM PCB
REMYE
MIC PCB
HT-DB360 OPTION
MIC PCB
HEADPHONE PCB
HT-DB360 OPTION
NEW DSP LED
MODEL NAME: HT-DB120
FILE NAME: HT-DB120.F
DESIGNER: J.N.KIM
DATE: 2003.04.15

HT-DB120/TH-A25 FUNCTION BOARD CIRCUIT DIAGRAM




HT-DB120/TH-A25 JACK
SCART JACK
RCA JACK
HEADER20_SMD..1M/M_ST
JCN1
COMPO(OPTION)
S_C/COMP
V-GND
S_Y
Y/G
V-GND
Pr/R
Pb/B
V_GND
OPT_OUT
OPT_GND
OPT_IN
OPT-GND
F_R
+5V
F_L
S_GND
AUX_L
S_GND
AUX_R
SCRT1
AR_IN
AL_IN
B_GND
BLUE_IN
G_GND
GREEN_IN
R_GND
RED_IN
V_GND
CVBS_OUT
SCART_GND
AR_OUT
AGND
AL_OUT
AV_SW
AVS
FT_USE
FGND
RGB_BL
VI_GND
CVBS_IN
VW2
A/D-R
A/GND
A/D-R
AV_SW2
AV_SW1
V-AV SEL
SCART-MUTE
SCART-P/MUTE
TUNED
STEREO
-12V
+12V
VW1
MDATA
MCLK
T-LATCH
T-DATA
A/D-R
A/GND
A/D-L
T-R/CH
T/GND
T-L/CH
RDS_CLK
RDS_DATA
VW4
TUNED
ST
VCC
L/OUT
GND
R/OUT
CE
DI
CLK
DO
D/GND
RDS/CLK
RDS/DA
DIGITAL-IN
COMP
RGB
VIDEO_OUT
VIC4
BA7665FS_SMD
MUTEA
INA
GND
GND
GND
N.C
INB
MUTEB
VCC1
OUT1
OUTA2
VCC2
MIXOUT1
MIXOUT2
N.C
OUTB
SVJ5
SUPER_VIDEO
MODEL NAME: DB120
FILE NAME: DB120.J
DESIGNER: J.N.KIM
DATE: 2003.07.09


ALi M5705 (176 pin)
SP-3721A
SIC1
FAN8082_SMD(FAN8082)
CONNECTOR_7mils_SMD_ST_RP
HEADER_2mils_AH_SMD_RP
6MD_24P_FFC_AH_1MM
HEADER_3mils_SMD_AH_SP
HEADER_SMD_11P_AH_1mm
sled
FRONTEND


ES60XX
ES6028X DECOUPLING CAPACITORS
SDRAM DECOUPLING CAPACITORS
U13 ES6028F
HOST MEMORY
ATAPI INTERFACE
Core Frontend
OPIC1
AIC1
STAC9460(DAC)
U14
64MB SDRAM_M12L64164A
U11
U16
DVD_PS_OPT
330MA
2.85V
210MA
3.5V
AJS1
AJS2